ket No.: 394-1597

PATENT

2511

A. Zarabian

Group Art Unit:

Examiner:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Seiji SAWADA et al.

Serial No. 08/246,582

Filed: May 19, 1994

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY

DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Honorable Commissioner of Patents and Trademarks Washington, D. C. 20231

NOV 1 4 1994

Sir:

Noting the Office Action of October 21, 1994 wherein restriction has been required, Applicants hereby elect Group I (claims 1-5 and 9-33) for prosecution in the above-identified application.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

Stephen A. Becker

Registration No. 26,527

99 Canal Center Plaza, Suite 300 Alexandria, Virginia 22314 (703) 684-1111 SAB:cms
Date: November 9, 1994